Enrolln		H UNIVERSITY	
	Summer I	Examination-2015	
Subject	Name: Microcontroller & Inter	facing	
Subject	Code: 4TE04MCI1	Branch: B.Tech (EEE,EE	L,IC)
Semeste Instructi		6 Time: 2:30 To 5:30	Marks: 70
(2) (3)	Use of Programmable calculator of Instructions written on main answ Draw neat diagrams and figures (Assume suitable data if needed.		rohibited.
-1 a)	Attempt the following question How many Bits can be processed (A)4(B) 2(C) 8(D)16		(14)
b)	How many interrupts are there (A)4 (B) 2 (C) 3(D) 5	in the 8085?	
c)	What is the size of internal ROI (A)0 GB(B)128 BYTE(C) 4 KI		
d)	Which register pair is used as d (A) AB(B)BC(C) DE(D) HL	ata pointer in the 8085?	
e)	•	on Architecture? C)Oxford(D) None of the above	
f)	The size of accumulator in 805 (A)4 (B) 8(C)10(D) 16		
g)	What is size of internal RAM in (A)4MB(B) 4KB(C)0 GB(D)12		
h)			
i)	The number of timers in the 80: (A) 4 (B) 2 (C) 8 (D)		
j)	is default register bank (A)BANK-3(B)BANK-2(C) BA	•	
k)			
1)		interchange the nibbles of Accumulate WAP	tor is



Attem	pt any	four questions from Q-2 to Q-8	
Q-2		Attempt all questions	(14)
	a)	Draw the block diagram of 8085 microprocessor and explain each block in detail.	
	b)	Write a note on Addressing Modes of 8051	
Q-3		Attempt all questions	
	a)	Draw and explain Memory Map of 8051.	
	b)	Write a Program to Add two Words.	
Q-4		Attempt all questions	(14)
	a)	Explain PSW register of 8051 in detail.	
	b)	Differentiate between Microprocessor and Microcontroller.	
Q-5		Attempt all questions	(14)
	a)	Write a note on interrupts of 8051.	()
	b)	Write a program to arrange 10 Data Array in descending order stored in external memory location staring from 6000h to 6009h.	
Q-6		Attempt all questions	(14)
	a)	Draw and explain functional diagram of 8051.	
	b)	Write a program to generate square wave of on P1.0 of 1kHz. (XTAL=22MHz)	
Q-7		Attempt all questions	(14)
	a)	Draw and explain Internal Port Structure of 8051.	
	b)	Write a program to convert Decimal number placed in external memory location	
		7000h to hexadecimal and store the result into R0.	
Q-8		Attempt all questions	(14)
-	a)	Explain the interfacing of LCD with 8051 with necessary schematics and code for	. ,
		the interfacing?	
	b)	What is assembler directive? Explain any two assembly directives	

m) After division operation in 8051, reminder is stored in the _____.

What is the bit pattern of Accumulator after execution of MOV A, # - 128?

(A)Accumulator(B)B Register(C)PSW(D) Register bank

n)

